	Application No.	Applicant(s)	
M-4' F A II	09/681,268	NGAI, HENRY P.	
Notice of Allowability	Examiner	Art Unit	
	Dmitry Levitan	2662	
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.			
1. This communication is responsive to the amendment, filed 10/11/04.			
2. The allowed claim(s) is/are <u>2-20, renumbered as 1-19.</u>			
3. The drawings filed on 12 March 2001 are accepted by the Examiner.			
4.			
Attachment(s) 1. Notice of References Cited (PTO-892) 2. Notice of Draftperson's Patent Drawing Review (PTO-948) 3. Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	5. ☐ Notice of Informal P 6. ☐ Interview Summary Paper No./Mail Dat 8), 7. ☑ Examiner's Amendn 8. ☐ Examiner's Stateme 9. ☐ Other	(PTO-413), te nent/Comment	·

Amendment, filed on 10/11/04 has been entered. Claims 1-20 are allowed.

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Stuart Auvinen on 12/20/04.

The application has been amended as follows:

Claim 2 has been amended as shown in Attachment A.

The claim has been was amended to clarify the language by removing repetitive limitations.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dmitry Levitan whose telephone number is (571) 272-3093. The examiner can normally be reached on 8:30 to 4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Hassan Kizou can be reached on (571) 272-3088. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Dmitry Levitan Patent Examiner.

12/20/04.

SUPERVISORY PATENT EXAMINER
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Attachment A.

1. (canceled)

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- 2. (currently amended) <u>A bus-based network switch comprising:</u>
- a plurality of switch nodes, each switch node for receiving a data payload from an input port or for transmitting a data payload to an external network device through an output port;
 - a first bus for sending switch packets with the data payload in a first direction among
 the plurality of switch nodes, the first bus being divided into links, each link for
 transferring a switch packet between two of the switch nodes;
 - a second bus for sending switch packets with the data payload in a second direction

 among the plurality of switch nodes, the second bus being divided into links,

 each link for transferring the switch packet between two of the switch nodes;
- 20 <u>destination switch node in the plurality of switch nodes can be reached from any other switch node in the plurality of switch nodes both by following the continuous loop in the first direction and by following the continuous loop in the second direction; and</u>

wherein the plurality of switch nodes are arranged in a continuous loop wherein a

- a packet limiter that limits a number of switch nodes that the switch packet travels
 25 through from a source switch node that receives the data payload to the
 destination switch node that transmits the data payload from the switch packet to
 the output port;
 - The bus-based network switch of claim 1-wherein the packet limiter limits the number of switch nodes to half or less of the total number of switch nodes in the plurality of switch nodes,

whereby the switch packet travels through a limited number of the switch nodes and whereby the switch packet travels no more than half-way around the continuous loop of switch nodes.